

RAPID SACRIFICIAL GERMANIUM ETCHING USING XENON DIFLUORIDE

Garrett D. Cole and Elaine Behymer

Center for Micro- and Nanotechnologies, Lawrence Livermore National Laboratory,
7000 East Avenue, Livermore, California 94550, USA

ABSTRACT

We present a novel micromachining procedure employing the noble gas halide, xenon difluoride (XeF_2), to rapidly undercut a sacrificial layer comprised of low-temperature deposited amorphous germanium (α -Ge). As a proof of concept, this process is utilized to fabricate electrostatically-actuated suspended Bragg mirrors applicable to wavelength-tunable surface-normal photonic devices. Exploiting XeF_2 as the release etchant, we observe a truly remarkable lateral etch rate in excess of $150 \mu\text{m}/\text{min}$ for a $1\text{-}\mu\text{m}$ -thick sacrificial α -Ge film. Given the rapid nature of this etch, we envision this procedure to be a promising approach for the lateral undercutting of large-area microdevices.

INTRODUCTION

The sacrificial etch method is a fundamental technique for the construction of microelectromechanical systems (MEMS). Within this framework, processes employing gas-phase etchants such as XeF_2 , bromine trifluoride [1], and HF vapor [2] have proven quite successful; particularly in the case of XeF_2 for sacrificial silicon etching. As opposed to wet chemistry mediated undercutting, these processes enable rapid lateral etching without inducing capillary forces, thus eliminating the need for post-release critical point drying (CPD) and enabling efficient penetration into narrow micro/nanoscale vias. In this manuscript we present a novel micromachining procedure utilizing XeF_2 to selectively etch a sacrificial α -Ge film. When compared with sacrificial silicon etching, this process exhibits a significant enhancement in the achievable lateral etch rate. Additionally, the fabrication sequence may be tailored to incorporate low temperature deposition steps, ranging from thermal or electron-beam evaporation of α -Ge at room temperature (enabling deposition rates of 100 \AA/s [3]), to CVD processes producing high quality polycrystalline germanium films at temperatures below 450°C [4].

GAS-PHASE GERMANIUM ETCHING

The results from a preliminary investigation of the lateral etch rate of α -Ge in XeF_2 are presented in Fig. 1. For this experiment the α -Ge is deposited by room temperature electron-beam evaporation onto single-crystal GaAs substrates. This procedure results in extremely smooth films with surface roughness values ranging from $8\text{--}11 \text{ \AA}$. Following evaporation, a SiN_x structural film is deposited via PECVD at 200°C . A pattern consisting of circular etch vias and orthogonal stripes is then transferred into the SiN_x with dilute HF. Finally, the α -Ge layer is selectively undercut in a pulsed XeF_2 etching system [5]. From the circular vias we measure a lateral etch rate of approximately $55 \mu\text{m}/\text{min}$ for the $1\text{-}\mu\text{m}$ -thick α -Ge sample. This value is comparable to that recently presented for crystalline germanium samples [6] and largely exceeds the maximum typical silicon etch rate of approximately $10 \mu\text{m}/\text{min}$ [7]. However, for the $30\text{-}\mu\text{m}$ -wide orthogonal stripes surrounding the circular apertures (see inset image in Fig. 1), we measure a remarkable lateral etch rate of nearly $160 \mu\text{m}/\text{min}$. Given the rapid nature of this process, we propose that XeF_2 etching of germanium is a promising approach for undercutting large-area devices or for the lift-off of continuous films from a potentially re-usable handle substrate, as in the epitaxial liftoff process pioneered by Yablonovitch [8].

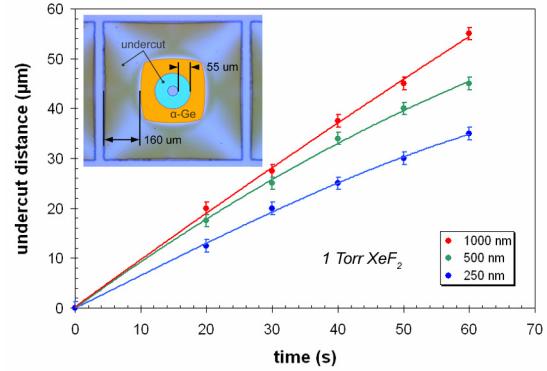


Figure 1: Undercut distance as a function of time for various thickness α -Ge films. The data corresponds to the position of the lateral etch front with respect to the $40\text{-}\mu\text{m}$ -diameter etch vias. Inset: micrograph of $1\text{-}\mu\text{m}$ α -Ge sample after 60 s of etching.

SUSPENDED DIELECTRIC BRAGG MIRRORS

The viability of our novel micromachining process is demonstrated through the development of electrostatically-actuated suspended dielectric Bragg reflectors (see Fig. 2), applicable to wavelength-tunable vertical-cavity lasers (VCSELs) [9,10] and amplifiers [11]. The detailed fabrication sequence for these devices may be found in [12]. Although XeF_2 has been applied extensively in silicon-based microfabrication, to the best of our knowledge, this is the first use of this etchant for the development of MEMS with a germanium sacrificial layer, as well as the first use of XeF_2 for MEMS on a compound semiconductor substrate.

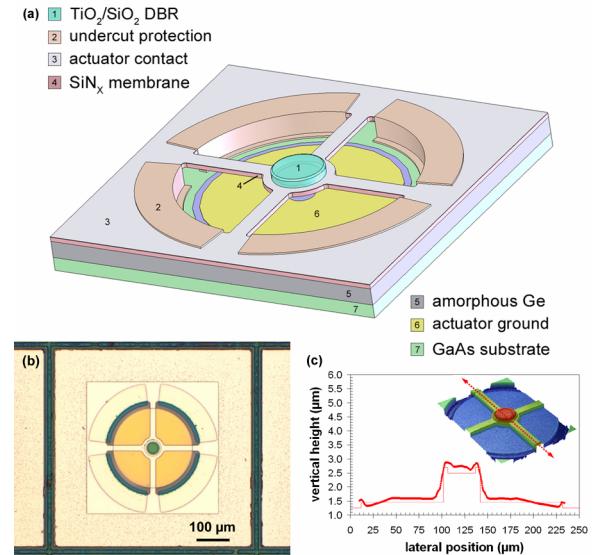


Figure 2: (a) Solid model of the suspended Bragg mirror. (b) Optical micrograph of a completed micromirror. (c) Line scan recorded with an optical profilometer; the inset includes the original 3-D mapping and indicates the path of the 2-D plot. The ideal profile is depicted along with the measurement. A minor upward bowing extends the nominal 1000-nm air gap to 1150 nm .

As with sacrificial silicon etching, this process results in excellent selectivity over most metals, dielectrics, polymers, etc. Additionally, the use of the non-plasma etch eliminates surface damage induced by ion bombardment, while the simplicity of the etching system allows for *in situ* viewing of the lateral etch front. Compared with previous implementations [11], the fabrication of the suspended mirror structures is greatly simplified through: 1) the elimination of the CPD process, 2) the ability to monitor the lateral etch front beneath the transparent mirror structure, and 3) the largely reduced release etch time. In fact, for the devices presented in this manuscript, the total time required to successfully undercut an 80- μm diameter membrane is less than 20 seconds (achieved through two 10-s pulses or one single 20-s etch pulse).

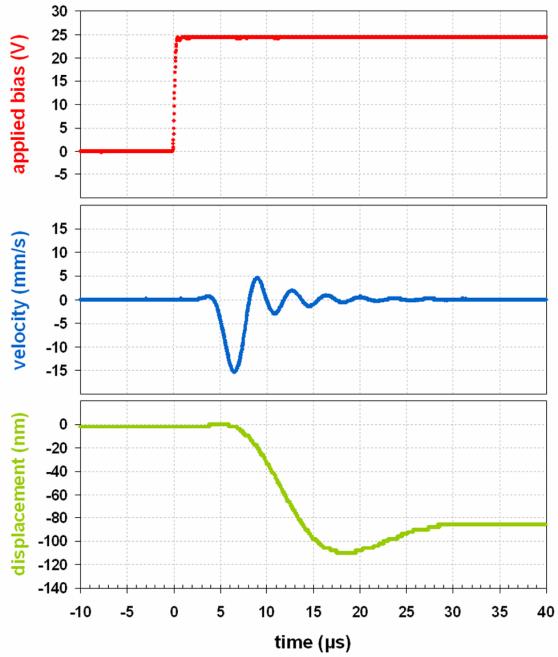


Figure 3: Step response (atmospheric pressure) for a 60 μm diameter membrane with 70 μm long by 20 μm wide tethers at a bias of 24 V; negative values indicate vertical motion towards the substrate. Ringing leads to a settling time of just under 30 μs .

Following fabrication we investigate the static and dynamic characteristics of the suspended reflector using a laser-Doppler-vibrometer-based MEMS motion characterization system [13]. Fig. 3 presents the step response of the suspended mirror structure, while the compiled displacement versus bias data are included in Fig. 4. It is important to note that the step response exhibits temporal smoothing due to the limited 3-dB bandwidth of the vibrometer (250 kHz for velocity and 50 kHz for displacement). The settling time of this structure may be reduced by optimizing the geometry of the actuator to achieve sufficient squeeze-film damping for a near critically-damped response [14].

From the theoretical fit of the static deflection data, the estimated snap-down voltage (displacement) is 30.9 V (447 nm) with the best fit achieved for an air-gap thickness of 1120 nm. This value compares well with the profilometer measurements of the air-gap thickness (1150 nm). Assuming this mirror is incorporated on a GaAs-based 1- λ semiconductor cavity with a hypothetical center wavelength of 1000 nm, the measured deflection would result in a wavelength tuning range of 37 nm at 30 V bias (this calculation includes the penetration depth for a 25.5 period GaAs/AlAs bottom mirror).

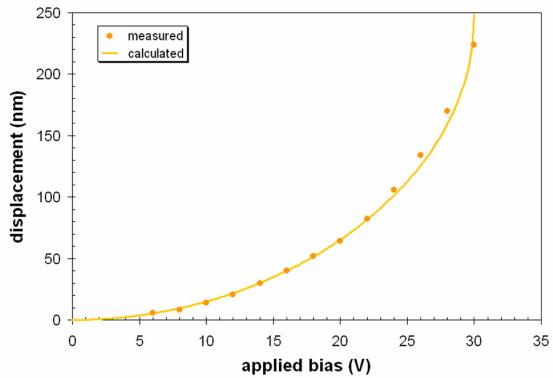


Figure 4: Static deflection characteristics of the suspended mirror. The theoretical curve is generated from the iterative solution of a one-dimensional electromechanical model [12]. Current-voltage measurements demonstrate leakage currents below 20 pA at 30 V.

SUMMARY AND FUTURE WORK

We have demonstrated a novel micromachining process exploiting XeF₂ to rapidly remove an evaporated α -Ge sacrificial layer. For large etch apertures we record undercut rates exceeding 150 $\mu\text{m}/\text{min}$. Further investigation will be necessary to quantify the lateral etch rate for narrow (sub-micron) vias. Finally, this procedure has been utilized to demonstrate high-performance arrays of electrostatically actuated suspended Bragg reflectors.

ACKNOWLEDGMENTS

This work performed under the auspices of the U.S. DOE by Lawrence Livermore National Laboratory under Contract DE-AC52-07NA27344. GDC thanks Professor Roger T. Howe of Stanford University for a number of insightful discussions.

REFERENCES

- [1] T. Yao, X. Yang, Y.C. Tai, *Sens. Actuators A* **97**, 652 (2002).
- [2] Y.I. Lee, K.H. Park, J. Lee, C.S. Lee, H.J. Yoo, C.J. Kim, Y.S. Yoon, *J. MEMS* **6**, 226 (1997).
- [3] M.L. Rappaport, B. Berkovitz, *J. Vac. Sci. Technol.* **21**, 102 (1982).
- [4] A.E. Franke, J.M. Heck, T.J. King, R.T. Howe, *J. MEMS* **12**, 160 (2003).
- [5] P.B. Chu, J.T. Chen, R. Yeh, G. Lin, J.C.P. Huang, B.A. Warneke, S.J. Pister, *Proc. Transducers '97*, Chicago, IL, USA, paper 2D3.01, p. 665.
- [6] G. Xuan, T.N. Adam, J. Suehle, E. Fitzgerald, P. Lv, N. Sustersic, M.J. Coppinger, J. Kolodzey, *Proc. ISTD 2006*, Princeton, NJ, paper 5P.44.
- [7] K.R. Williams, R.S. Muller, *J. MEMS* **5**, 256 (1996).
- [8] E. Yablonovitch, D.M. Hwang, T.J. Gmitter, L.T. Florez, J.P. Harbison, *Appl. Phys. Lett.* **56**, 2419 (1990).
- [9] M. Maute, B. Kögel, G. Bohm, P. Meissner, M.-C. Amann, *IEEE Photon. Technol. Lett.* **18**, 688 (2006).
- [10] M.C. Huang, K.B. Cheng, Y. Zhou, A.P. Pisano, C.J. Chang-Hasnain, *IEEE J. Sel. Top. Quantum Electron.* **13**, 374 (2007).
- [11] G.D. Cole, E.S. Björlin, C.S. Wang, N.C. MacDonald, J.E. Bowers, *IEEE Photon. Technol. Lett.* **17**, 2526 (2005).
- [12] G.D. Cole, E. Behymer, L.L. Goddard, T.C. Bond, *J. Vac. Sci. Technol. B* **26**, 593 (2008).
- [13] K.L. Turner, P.G. Hartwell, N.C. Macdonald, *Proc. Transducers '99*, Sendai, Japan, p. 1144.
- [14] G.D. Cole, J.E. Bowers, K.L. Turner, N. C. MacDonald, *IEEE/LEOS Optical MEMS 2005*, Oulu, Finland, paper F4.